

System-Side Impedance Track™ Fuel Gauge

1 INTRODUCTION

1.1 FEATURES

- Battery Fuel Gauge for 1-Series Li-Ion Applications
- Resides on System Main Board
 - Works With Embedded or Removable Battery Packs
- Uses PACK+, PACK–, and T Battery Terminals
- Microcontroller Peripheral Provides:
 - Accurate Battery Fuel Gauging
 - Internal Temperature Sensor for Battery Temperature Reporting
 - *Battery Low* Interrupt Warning
 - *Battery Insertion* Indicator
 - *Configurable Level of State of Charge (SOC) Interrupts*
 - *State of Health* Indicator
 - 96 Bytes of Non-Volatile Scratch-Pad FLASH
- Battery Fuel Gauge Based on Patented Impedance Track™ Technology
 - Models the Battery Discharge Curve for Accurate Time-to-Empty Predictions
 - Automatically Adjusts for Battery Aging, Battery Self-Discharge, and Temperature/Rate Inefficiencies
 - Low-Value Sense Resistor (10 mΩ or Less)
- 400-kHz I²C™ Interface for Connection to System Microcontroller Port
- In a 12-Pin NanoFree™ (CSP) Packaging

1.2 APPLICATIONS

- Smartphones
- PDAs
- Digital Still and Video Cameras
- Handheld Terminals
- MP3 or Multimedia Players

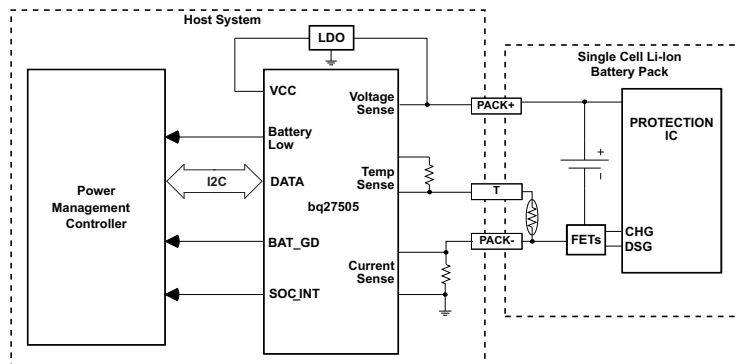
1.3 DESCRIPTION

The Texas Instruments bq27505 system-side Li-Ion battery fuel gauge is a microcontroller peripheral that provides fuel gauging for single-cell Li-Ion battery packs. The device requires little system microcontroller firmware development. The bq27505 resides on the system's main board and manages an embedded battery (non-removable) or a removable battery pack.

The bq27505 uses the patented Impedance Track™ algorithm for fuel gauging, and provides information such as remaining battery capacity (mAh), state-of-charge (%), run-time to empty (min), battery voltage (mV), temperature (°C) and state of health (%).

Battery fuel gauging with the bq27505 requires only PACK+ (P+), PACK– (P–), and Thermistor (T) connections to a removable battery pack or embedded battery circuit. The CSP option is a 12-ball package in the dimensions of 2,43 mm × 1,96 mm with 0,5 mm lead pitch. It is ideal for space constrained applications.

TYPICAL APPLICATION



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I²C is a trademark of Philips Electronics.



2 DEVICE INFORMATION

2.1 AVAILABLE OPTIONS

PART NUMBER	FIRMWARE VERSION	PACKAGE ⁽¹⁾	T _A	COMMUNICATION FORMAT	TAPE & REEL QUANTITY
bq27505YZGR-J3	2.20	CSP-12	-40°C to 85°C	I ² C	3000
bq27505YZGT-J3					250

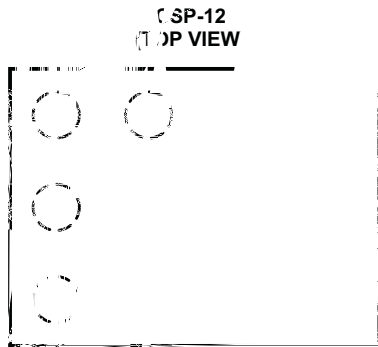
- (1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI website at www.ti.com.

2.2 DISSIPATION RATINGS

PACKAGE	THERMAL	RESISTANCE ^{(1) (2)}	POWER RATING T _A = 25°C	DERATING FACTOR ABOVE ^{(1) (2)} T _A = 25°C
YZG	q _{JA} = 89°C/W	q _{JB} = 35°C/W	1.1 W	12 mW/°C

- (1) Measured with high-K board.
(2) Maximum power dissipation is a function of T_{J(max)}, q_{JA} and T_A. The maximum allowable power dissipation at any allowable ambient temperature is PD = (T_{J(max)} - T_A) / q_{JA}.

2.3 PIN ASSIGNMENT


Table 2-1. PIN FUNCTIONS

PIN		TYPE ⁽¹⁾	DESCRIPTION
NAME	NO.		
SRP	A1	IA	Analog input pin connected to the internal coulomb counter where SRP is nearest the PACK- connection. Connect to 5-mΩ to 20-mΩ sense resistor.
SRN	B1	IA	Analog input pin connected to the internal coulomb counter where SRN is nearest the Vss connection. Connect to 5-mΩ to 20-mΩ sense resistor.
BAT_LOW	C1	O	Battery Low output indicator. Active <i>high</i> by default, though polarity can be configured through the [BATL_POL] bit of Operation Configuration . Push-pull output.
Vss	D1	P	Device ground
BAT_GD	A2	O	Battery-good indicator. Active- <i>low</i> by default, though polarity can be configured through the [BATG_POL] bit of Operation Configuration . Push-pull output.
SOC_INT	B2	I/O	SOC state interrupts output. Generate a pulse under the conditions specified by Table 5-5 . Open drain output.
BAT	C2	I	Cell-voltage measurement input. ADC input. Recommend 4.8V maximum for conversion accuracy.
Vcc	D2	P	Processor power input. Decouple with minimum 0.1mF ceramic capacitor.
SDA	A3	I/O	Slave I ² C serial communications data line for communication with system (Master). Open-drain I/O. Use with 10kΩ pull-up resistor

3.8 ADC (TEMPERATURE AND CELL MEASUREMENT) CHARACTERISTICS

$T_A = -40^{\circ}\text{C}$ to 85°C , $2.4\text{ V} < V_{CC} < 2.6\text{ V}$; typical values at $T_A = 25^{\circ}\text{C}$ and $V_{CC} = 2.5\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
$V_{IN(ADC)}$	Input voltage range		-0.2		1	V
t_{ADC_CONV}	Conversion time				125	ms
	Resolution		14		15	bits
$V_{OS(ADC)}$	Input offset			1		mV
Z_{ADC1}	Effective input resistance (TS) ⁽¹⁾		8			MΩ
Z_{ADC2}	Effective input resistance (BAT) ⁽¹⁾	bq27505 not measuring cell voltage	8			MΩ
		bq27505 measuring cell voltage		100		kΩ
$I_{lkg(ADC)}$	Input leakage current ⁽¹⁾				0.3	mA

(1) Specified by design. Not tested in production.

3.9 DATA FLASH MEMORY CHARACTERISTICS

$T_A = -40^{\circ}\text{C}$ to 85°C , $2.4\text{ V} < V_{CC} < 2.6\text{ V}$; typical values at $T_A = 25^{\circ}\text{C}$ and $V_{CC} = 2.5\text{ V}$ (unless otherwise noted)

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
t_{DR}	Data retention ⁽¹⁾		10			Years
	Flash-programming write cycles ⁽¹⁾		20,000			Cycles
$t_{WORDPROG}$	Word programming time ⁽¹⁾				2	ms
I_{CCPROG}	Flash-write supply current ⁽¹⁾			5	10	mA

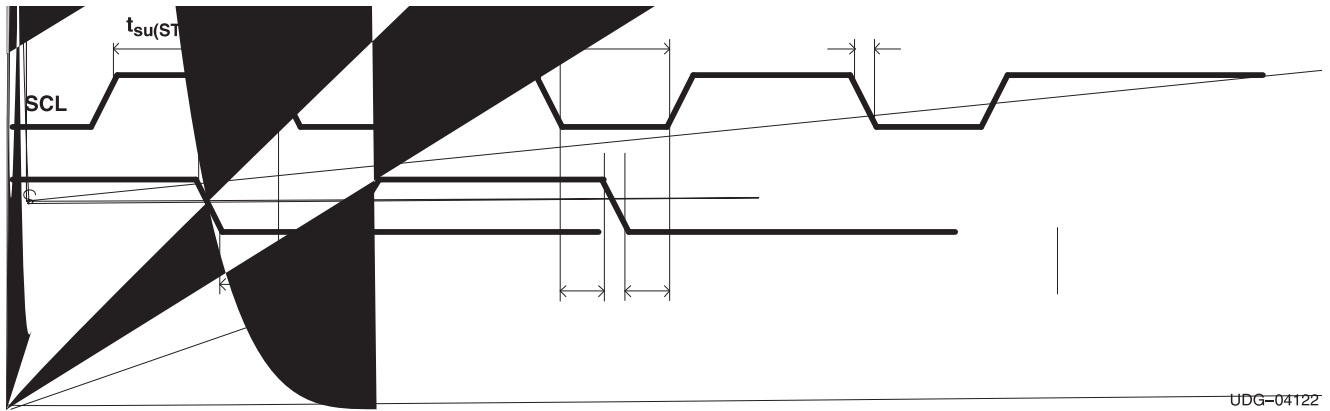
Cycles

Cycles

3.10 I²C-COMPATIBLE INTERFACE COMMUNICATION TIMING CHARACTERISTICS

T_A = -40°C to 85°C, 2.4 V < V_{CC} < 2.6 V; typical values at T_A = 25°C and V_{CC} = 2.5 V (unless otherwise noted)

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _r	SCL/SDA rise time			300	ns
t _f	SCL/SDA fall time			300	ns
t _{w(H)}	SCL pulse duration (high)	600			ns
t _{w(L)}	SCL pulse duration (low)	1.3			ms
t _{60(STA)}	Setup for repeated start	600			ns
t _{Start to Td}	Start to first	600			



4 GENERAL DESCRIPTION

The bq27505 accurately predicts the battery capacity and other operational characteristics of a single Li-based rechargeable cell. It can be interrogated by a system processor to provide cell information, such as time-to-empty (TTE), time-to-full (TTF) and state-of-charge (SOC) as

4.1 DATA COMMANDS

4.1.1 STANDARD DATA COMMANDS

The bq27505 uses a series of 2-byte standard commands to enable system reading and writing of battery information. Each standard command has an associated command-code pair, as indicated in Table 4-1. Because each command consists of two bytes of data, two consecutive I²C transmissions must be executed both to initiate the command function, and to read or write the corresponding two bytes of data. Additional options for transferring data, such as spooling, are described in Section of **Communication**. Standard commands are accessible in NORMAL operation. Read/Write permissions depend on the active access mode, SEALED or UNSEALED (for details on the SEALED and UNSEALED states, refer to Section 4.4, Access Modes.)

Table 4-1. Standard Commands

NAME		COMMAND CODE	UNITS	SEALED ACCESS	UNSEALED ACCESS
Control()	CNTL	0x00 / 0x01	N/A	R/W	R/W
AtRate()	AR	0x02 / 0x03	mA	R/W	R/W
AtRateTimeToEmpty()	ARTTE	0x04 / 0x05	Minutes	R	R/W
AtRateTimeToEmpty() (0x07)	ARTTE	0x06 / 0x07	Minutes	R	R/W
Charge()					

4.1.1.1.1 CONTROL_STATUS: 0x0000

Instructs the fuel gauge to return status information to control addresses 0x00/0x01. The status word includes the following information.

Table 4-3. CONTROL_STATUS Bit Definitions

	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
High byte	–	FAS	SS	CSV	CCA	BCA	OCVMDCOMP	OCVFAIL
Low byte	INITCOMP	HIBERNATE	SNOOZE	SLEEP	LDMD	RUP_DIS	VOK	QEN

FAS = Status bit indicating the bq27505 is in FULL ACCESS SEALED state. Active when set.

SS = Status bit indicating the bq27505 is in SEALED state. Active when set.

CSV = Status bit indicating a valid data flash checksum has been generated. Active when set.

CCA = Status bit indicating the bq27505 Coulomb Counter Calibration routine is active. The CCA routine will take place approximately 1 minute after the initialization. Active when set.

BCA = Status bit indicating the bq27505 board calibration routine is active. Active when set.

OCVMDCOMP = Status bit indicating the bq27505 has executed the OCV command. This bit can only be set with battery's presence. True when set.

OCVFAIL = Status bit indicating bq27505 OCV reading is failed due to the current. This bit can only be set with battery's presence. True when set.

INITCOMP = Initialization completion bit indicating the initialization completed. This bit can only be set with battery's presence. True when set.

HIBERNATE = Status bit indicating a request for entry into HIBERNATE from SLEEP mode. True when set. Default is 0.

SNOOZE = Status bit indicating the bq27505 SLEEP+ mode is enabled. True when set.

SLEEP = Status bit indicating the bq27505 is in SLEEP mode. True when set.

LDMD = Status bit indicating the bq27505 Impedance Track™ algorithm is using constant-power mode. True when set. Default is 0 (constant-current mode).

RUP_DIS = Status bit indicating the bq27505 Ra table updates are disabled. Updates disabled when set.

VOK = Status bit indicating the bq27505 voltages are okay for Qmax. True when set.

QEN = Status bit indicating the bq27505 Qmax updates enabled. True when set.

4.1.1.1.2 DEVICE_TYPE: 0x0001

Instructs the fuel gauge to return the device type to addresses 0x00/0x01.

4.1.1.1.3 FW_VERSION: 0x0002

Instructs the fuel gauge to return the firmware version to addresses 0x00/0x01.

4.1.1.1.4 HW_VERSION: 0x0003

Instructs the fuel gauge to return the hardware version to addresses 0x00/0x01.

4.1.1.1.5 DF_CHECKSUM: 0x0004

Instructs the fuel gauge to compute the checksum of the data flash memory. The checksum value is written and returned to addresses 0x00/0x01 (UNSEALED mode only). The checksum will not be calculated in SEALED mode; however, the checksum value can still be read.

4.1.1.1.6 PREV_MACWRITE: 0x0007

Instructs the fuel gauge to return the previous command written to addresses 0x00/0x01.

4.1.1.1.7 CHEM_ID: 0x0008

Instructs the fuel gauge to return the chemical identifier for the Impedance Track™ configuration to addresses 0x00/0x01.

4.1.1.1.8 BOARD_OFFSET: 0x0009

Instructs the fuel gauge to compute the coulomb counter offset with internal short and then without internal short applied across the sensing resistor (SR) inputs. The difference between the two measurements is

the board offset. After a delay of approximately 32 seconds, this offset value is returned to addresses 0x00/0x01 and written to data flash. The CONROL STATUS [BCA] is also set. The usF5 10 Tf 423. /Xf4 Do Q iTj 21.5

4.1.1.1.18 SEALED: 0x0020

Instructs the fuel gauge to transition from the UNSEALED state to the SEALED state. The fuel gauge must always be set to the SEALED state for use in end equipment.

4.1.1.1.19 IT_ENABLE: 0x0021

This command forces the fuel gauge to begin the Impedance Track™ algorithm, sets the *IT Enable* to 0x01 and causes the [VOK] and [QEN] flags to be set in the CONTROL_STATUS register. [VOK] is cleared if the voltages are not suitable for a Qmax update. This command is only available when the fuel gauge is UNSEALED.

4.1.1.1.20 IT_DISABLE: 0x0023

This command disables the fuel gauge the Impedance Track™ algorithm, clears the *IT Enable* to 0x00 and causes the [QEN] flags to be cleared in the CONTROL_STATUS register. This command is only available when the fuel gauge is UNSEALED.

4.1.1.1.21 CAL_MODE: 0x0040

This command instructs the fuel gauge to enter calibration mode. This command is only available when the fuel gauge is UNSEALED.

4.1.1.1.22 RESET: 0x0041

This command instructs the fuel gauge to perform a full reset. This command is only available when the fuel gauge is UNSEALED.

4.1.1.2 AtRate(): 0x02/0x03

The *AtRate()* read/write-word function is the first half of a two-function command set used to set the *AtRate* value used in calculations made by the *AtRateTimeToEmpty()* function. The *AtRate()* units are in mA.

The *AtRate()* value is a signed integer, with negative values interpreted as a discharge current value. The *AtRateTimeToEmpty()* function returns the predicted operating time at the *AtRate* value of discharge. The default value for *AtRate()* is zero and forces *AtRateTimeToEmpty()* to return 65,535. Both the *AtRate()* and *AtRateTimeToEmpty()* commands must only be used in NORMAL mode.

4.1.1.3 AtRateTimeToEmpty(): 0x04/0x05

This read-word function returns an unsigned integer value of the predicted remaining operating time if the battery is discharged at the *AtRate()* value in minutes with a range of 0 to 65,534. A value of 65,535 indicates *AtRate()* = 0. The fuel gauge updates *AtRateTimeToEmpty()* within 1 s after the system sets the *AtRate()* value. The fuel gauge automatically updates *AtRateTimeToEmpty()* based on the *AtRate()* value every 1 s. Both the *AtRate()* and *AtRateTimeToEmpty()* commands must only be used in NORMAL mode.

4.1.1.4 Temperature(): 0x06/0x07

This read/write-word function returns an unsigned integer value of the temperature in units of 0.1 K measured by the fuel gauge. Write temperature to the gauge when the [WRTEMP] is 1.

4.1.1.5 Voltage(): 0x08/0x09

This read-word function returns an unsigned integer value of the measured cell-pack voltage in mV with a range of 0 to 6000 mV.

4.1.1.6 **Flags():** 0x0a/0x0b

This read-word function returns the contents of the fuel-gauge status register, depicting the current operating status.

Table 4-4. Flags Bit Definitions

	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
High byte	OTC	OTD	–	–	CHG_INH	XCHG	FC	CHG
Low byte	–	–	OCV_GD	WAIT_ID	BAT_DET	SOC1	SYSDOWN	DSG

OTC = Overtemperature in charge condition is detected. True when set.

OTD = Overtemperature in discharge condition is detected. True when set.

CHG_INH = Charge inhibit: unable to begin charging (temperature outside the range [*Charge Inhibit Temp Low, Charge Inhibit Temp High*]). True when set.

XCHG = Charge suspend alert (temperature outside the range [*Suspend Temperature Low, Suspend Temperature High*]). True when set.

FC = Full-charged condition reached. Set when charge termination condition is met. (RMFCC=1; Set FC_Set % = -1% when RMFCC = 0). True when set

CHG = (Fast) charging allowed. True when set.

OCV_GD = Good OCV measurement taken. True when set.

WAIT_ID = Waiting to identify inserted battery. True when set.

BAT_DET = Battery detected. True when set.

SOC1 = State-of-charge threshold 1 (**SOC1 Set**) reached. The flag is enabled when BL_INT bit in **Operation Configuration B** is set. True when set.

SysDown = SystemDown bit indicating the system shut down. True when set

DSG = Discharging detected. True when set.

4.1.1.7 **NominalAvailableCapacity():** 0x0c/0x0d

This read-only command pair returns the uncompensated (less than C/20 load) battery capacity remaining. Units are mAh.

4.1.1.8 **FullAvailableCapacity():** 0x0e/0x0f

This read-only command pair returns the uncompensated (less than C/20 load) capacity of the battery when fully charged. Units are mAh. *FullAvailableCapacity()* is updated at regular intervals, as specified by the IT algorithm.

4.1.1.9 **RemainingCapacity():** 0x10/0x11

This read-only command pair returns the compensated battery capacity remaining. Units are mAh.

4.1.1.10 **FullChargeCapacity():** 0x12/13

This read-only command pair returns the compensated capacity of the battery when fully charged. Units are mAh. *FullChargeCapacity()* is updated at regular intervals, as specified by the IT algorithm.

4.1.1.11 **AverageCurrent():** 0x14/0x15

This read-only command pair returns a signed integer value that is the average current flow through the sense resistor. It is updated every 1 second. Units are mA.

4.1.1.12 **TimeToEmpty():** 0x16/0x17

This read-only function returns an unsigned integer value of the predicted remaining battery life at the present rate of discharge, in minutes. A value of 65,535 indicates battery is not being discharged.

4.1.1.13 **TimeToFull():** 0x18/0x19

This read-only function returns an unsigned integer value of predicted remaining time until the battery reaches full charge, in minutes, based upon *AverageCurrent()*. The computation accounts for the taper current time extension from the linear TTF computation based on a fixed *AverageCurrent()* rate of charge accumulation. A value of 65,535 indicates the battery is not being charged.

4.1.1.14 StandbyCurrent(): 0x1a/0x1b

This read-only function returns a signed integer value of the measured standby current through the sense resistor. The *StandbyCurrent()* is an adaptive measurement. Initially it reports the standby current programmed in *Initial Standby*, and after spending several seconds in standby, reports the measured standby current.

The register value is updated every 1 second when the measured current is above the *Deadband* and is less than or equal to $2 \times$ *Initial Standby*. The first and last values that meet this criteria are not averaged in, since they may not be stable values. To approximate a 1 minute time constant, each new *StandbyCurrent()* value is computed by taking approximate 93% weight of the last standby current and approximate 7% of the current measured average current.

4.1.1.15 StandbyTimeToEmpty(): 0x1c/0x1d

This read-only function returns an unsigned integer value of the predicted remaining battery life at the standby rate of discharge, in minutes. The computation uses *Nominal Available Capacity* (NAC), the uncompensated remaining capacity, for this computation. A value of 65,535 indicates battery is not being discharged.

4.1.1.16 MaxLoadCurrent(): 0x1e/0x1f

This read-only function returns a signed integer value, in units of mA, of the maximum load conditions. The *MaxLoadCurrent()* is an adaptive measurement which is initially reported as the maximum load current programmed in *Initial Max Load Current*. If the measured current is ever greater than *Initial Max Load Current*, then *MaxLoadCurrent()* updates to the new current. *MaxLoadCurrent()* is reduced to the average of the previous value and *Initial Max Load Current* whenever the battery is charged to full after a previous discharge to an SOC less than 50%. This prevents the reported value from maintaining an unusually high value.

4.1.1.17 MaxLoadTimeToEmpty(): 0x20/0x21

This read-only function returns an unsigned integer value of the predicted remaining battery life at the maximum load current discharge rate, in minutes. A value of 65,535 indicates that the battery is not being discharged.

4.1.1.18 AvailableEnergy(): 0x22/0x23

This read-only function returns an unsigned integer value of the predicted charge or energy remaining in the battery. The value is reported in units of mWh.

4.1.1.19 AveragePower(): 0x24/0x25

This read-only function returns a signed integer value of the average power during battery charging and discharging. It is negative during discharge and positive during charge. A value of 0 indicates that the battery is not being discharged. The value is reported in units of mW.

4.1.1.20 TimeToEmptyAtConstantPower(): 0x26/0x27

This read-only function returns an unsigned integer value of the predicted remaining operating time if the battery is discharged at the *AveragePower()* value in minutes. A value of 65,535 indicates *AveragePower() = 0*. The fuel gauge automatically updates *TimeToEmptyatContantPower()* based on the *AveragePower()* value every 1 s.

Table 4-5. Extended Data Commands (continued)

NAME		COMMAND CODE	UNITS	SEALED ACCESS ⁽¹⁾ (2)	UNSEALED ACCESS ⁽¹⁾ (2)
<i>BlockDataControl()</i>	DFDCNTL	0x61	N/A	N/A	R/W
<i>DeviceNameLength()</i>	DNAMELEN	0x62	N/A	R	R
<i>DeviceName()</i>	DNAME	0x63...0x69	N/A	R	R
<i>ApplicationStatus()</i>	APPSTAT	0x6a	N/A	R	R
Reserved	RSVD	0x6b...0x7f	N/A	R	R

4.1.2.1 *DesignCapacity()*: 0x3c/0x3d

SEALED and UNSEALED Access: This command returns the value is stored in **Design Capacity** and is expressed in mAh. This is intended to be the theoretical or nominal capacity of a new pack, but has no bearing on the operation of the fuel gauge functionality.

4.1.2.2 *DataFlashClass()*: 0x3e

UNSEALED Access: This command sets the data flash class to be accessed. The class to be accessed must be entered in hexadecimal.

SEALED Access: This command is not available in SEALED mode.

4.1.2.3 *DataFlashBlock()*: 0x3f

UNSEALED Access: This command sets the data flash block to be accessed. When 0x00 is written to *BlockDataControl()*, *DataFlashBlock()* holds the block number of the data flash to be read or written. Example: writing a 0x00 to *DataFlashBlock()* specifies access to the first 32-byte block, a 0x01 specifies access to the second 32-byte block, and so on.

SEALED Access: This command directs which data flash block is accessed by the *BlockData()* command. Writing a 0x00 to *DataFlashBlock()* specifies that the *BlockData()* command transfers authentication data. Issuing a 0x01 or 0x02 instructs the *BlockData()* command to transfer **Manufacturer Info Block A** or **B**, respectively.

4.1.2.4 *BlockData()*: 0x40...0x5f

UNSEALED Access: This data block is the remainder of the 32 byte data block when accessing data flash.

SEALED Access: This data block is the remainder of the 32 byte data block when accessing **Manufacturer Block Info A or B**.

4.1.2.5 *BlockDataChecksum()*: 0x60

UNSEALED Access: This byte contains the checksum on the 32 bytes of block data read or written to data flash. The least-significant byte of the sum of the data bytes written must be complemented ($[255 - x]$, for x the least-significant byte) before being written to 0x60.

SEALED Access: This byte contains the checksum for the 32 bytes of block data written to **Manufacturer Info Block A or B**. The least-significant byte of the sum of the data bytes written must be complemented ($[255 - x]$, for x the least-significant byte) before being written to 0x60.

4.1.2.6 *BlockDataControl()*: 0x61

UNSEALED Access: This command is used to control data flash access mode. Writing 0x00 to this command enables *BlockData()* to access general data flash. Writing a 0x01 to this command enables SEALED mode operation of *DataFlashBlock()*.

SEALED Access: This command is not available in SEALED mode.

4.1.2.7 DeviceNameLength(): 0x62

4.3 MANUFACTURER INFORMATION BLOCKS

The bq27505 contains 96 bytes of user programmable data flash storage: **Manufacturer Info Block A**, and **Manufacturer Info Block B**. The method for accessing these memory locations is slightly different, depending on whether the device is in UNSEALED or SEALED modes.

When in UNSEALED mode and when 0x00 has been written to *BlockDataControl()*, accessing the manufacturer information blocks is identical to accessing general data flash locations. First, a *DataFlashClass()* command is used to set the subclass, then a *DataFlashBlock()* command sets the offset for the first data flash address within the subclass. The *BlockData()* command codes contain the referenced data flash data. When writing the data flash, a checksum is expected to be received by *BlockDataChecksum()*. Only when the checksum is received and verified is the data actually written to data flash.

As an example, the data flash location for **Manufacturer Info Block B** is defined as having a Subclass = 58 and an Offset = 32 through 63 (32 byte block). The specification of Class = System Data is not needed to address **Manufacturer Info Block B**, but is used instead for grouping purposes when viewing data flash info in the bq27505 evaluation software.

When in SEALED mode or when 0x01 *BlockDataControl()* does not contain 0x00, data flash is no longer available in the manner used in UNSEALED mode. Rather than issuing subclass information, a designated Manufacturer Information Block is selected with the *DataFlashBlock()* command. Issuing a 0x01 or 0x02 with this command causes the corresponding information block (A or B, respectively) to be transferred to the command space 0x40...0x5f for editing or reading by the system. Upon successful writing of checksum information to *BlockDataChecksum()*, the modified block is returned to data flash.
Note: **Manufacturer Info Block A** is read-only when in SEALED mode.

4.4 ACCESS MODES

The bq27505 provides three security modes (FULL ACCESS, UNSEALED, and SEALED) that control data flash access permissions, according to [Table 4-6](#). *Data Flash* refers to those data flash locations, specified in [Table 4-7](#), that are accessible to the user. *Manufacture Information* refers to the three 32-byte blocks.

Table 4-6. Data Flash Access

Security Mode	Data Flash	Manufacture Information
FULL ACCESS	R/W	R/W
UNSEALED	R/W	R/W
SEALED	None	R(A); R/W(B)

Although FULL ACCESS and UNSEALED modes appear identical, only FULL ACCESS allows the bq27505 to write access-mode transition keys.

4.5 SEALING/UNSEALING DATA FLASH

The bq27505 implements a key-access scheme to transition between SEALED, UNSEALED, and FULL-ACCESS modes. Each transition requires that a unique set of two keys be sent to the bq27505 via the *Control()* control command. The keys must be sent consecutively, with no other data being written to the *Control()* register in between. Note that to avoid conflict, the keys must be different from the codes presented in the *CNTL DATA* column of [Table 4-2](#) subcommands.

When in SEALED mode, the CONTROL_STATUS [SS] bit is set, but when the UNSEAL keys are correctly received by the bq27505, the [SS] bit is cleared. When the full-access keys are correctly received, then the CONTROL_STATUS [FAS] bit is cleared.

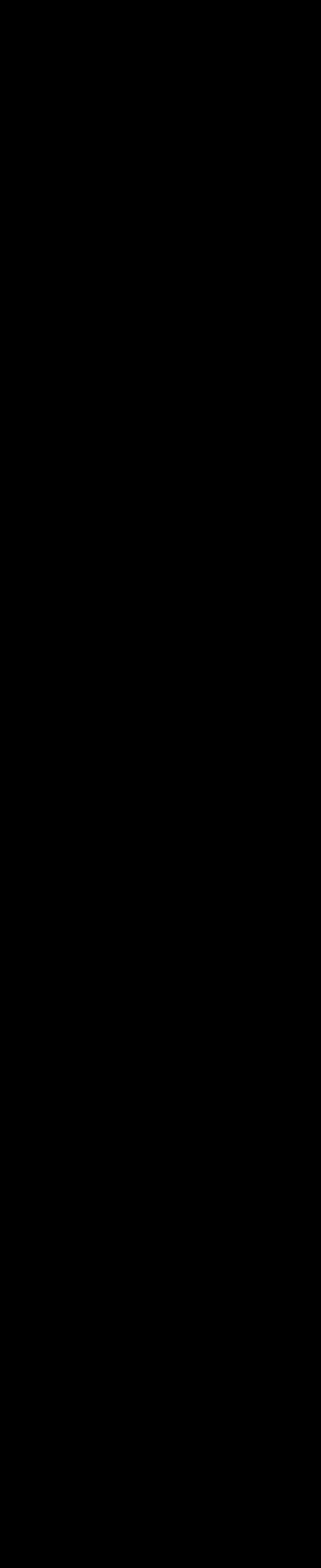
Both the sets of keys for each level are 2 bytes each in length and are stored in data flash. The UNSEAL key (stored at **Unseal Key 0** and **Unseal Key 1**) and the FULL-ACCESS key (stored at **Full-Access Key 0** and **Full-Access Key 1**) can only be updated when in FULL-ACCESS mode. The order of the keys is **Key 1** followed by **Key 0**. The order of the bytes entered through the *Control()* command is the reverse of what is read from the part. For example, if the **Key 1** and **Key 0** of the **Unseal Keys** returns 0x1234 and 0x5678, then the *Control()* should supply 0x3412 and 0x7856 to unseal the part.

4.6 DATA FLASH SUMMARY

[Table 4-7](#) summarizes the data flash locations available to the user, including their default, minimum, and maximum values.

Table 4-7. Data Flash Summary

Class	Subclass ID	Subclass	Offset	Name	Data Type	Min Value	Max Value	Default Value	Units
Configuration	2	Safety	0	OT Chg	I2	0	1200	550	0.1°C
Configuration	2	Safety	2	OT Chg Time	U1	0	60	2	s
Configuration	2	Safety	3	OT Chg Recovery	I2	0	1200	500	0.1°C
Configuration	2	Safety	5	OT Dsg	I2	0	1200	600	0.1°C
Configuration	2	Safety	7	OT Dsg Time	U1	0	60	2	s
Configuration	2	Safety	8	OT Dsg Recovery	I2	0	1200	550	0.1°C
Configuration	32	Charge Inhibit Temp Low	0	Charge Inhibit Temp Low	I2	-400	1200	0	0.1°C
Configuration	32	Charge Inhibit Temp High	2	Charge Inhibit Temp High	I2	-400	1200	450	0.1°C
Configuration	32	Temp Hysteresis	4	Temp Hys	I2	0	100	50	0.1°C
Configuration	34	Charge	2	Charging Voltage	I2	0	4600	4200	mV
Configuration	34	Charge	4	Delta Temp	I2	0	500	50	0.1°C





(5 mΩ to 20 mΩ typ.) between the SRP and SRN pins and in series with the cell. By integrating charge passing through the battery, the battery's SOC is adjusted during battery charge or discharge.

The total battery capacity is found by comparing states of charge before and after applying the load with the amount of charge passed. When an application load is applied, the impedance of the cell is measured by comparing the OCV obtained from a predefined function for present SOC with the measured voltage under load. Measurements of OCV and charge integration determine chemical state of charge and chemical capacity (Qmax). The initial Qmax values are taken from a cell manufacturers' data sheet multiplied by the number of parallel cells. It is also used for the value in **Design Capacity**. The bq27505 acquires and updates the battery-impedance profile during normal battery usage. It uses this profile, along with SOC and the Qmax value, to determine *FullChargeCapacity()* and *StateOfCharge()*, specifically for the present load and temperature. *FullChargeCapacity()* is reported as capacity available from a fully charged battery under the present load and temperature until *Voltage()* reaches the **Terminate Voltage**. *NominalAvailableCapacity()* and *FullAvailableCapacity()* are the uncompensated (no or light load) versions of *RemainingCapacity()* and *FullChargeCapacity()* respectively.

The bq27505 has two flags accessed by the *Flags()* function that warns when the battery's SOC has fallen to critical levels. When *RemainingCapacity()* falls below the first capacity threshold, specified in **SOC1 Set Threshold**, the *[SOC1] (State of Charge Initial)* flag is set. The flag is cleared once *RemainingCapacity()* rises above SOC1 Set Threshold.

After about 5 minutes in relaxation mode, the bq27505 attempts to take accurate OCV readings. An additional requirement of $dV/dt < 4$ mV/s is required for the bq27505 to perform Qmax updates. These updates are used in the Impedance Track™ algorithms. It is critical that the battery voltage be relaxed during OCV readings to and that the current is not be higher than C/20 when attempting to go into relaxation mode.

Quit Relax Time specifies the minimum time required for *AverageCurrent()* to remain above the **QuitCurrent** threshold before exiting relaxation mode.

5.2.8 Qmax 0 and Qmax 1

Generically called *Qmax*, these dynamic variables contain the respective maximum chemical capacity of the active cell profiles, and are determined by comparing states of charge before and after applying the load with the amount of charge passed. They also correspond to capacity at a very low rate of discharge, such as the C/20 rate. For high accuracy, this value is periodically updated by the bq27505 during operation. Based on the battery cell capacity information, the initial value of chemical capacity should be entered in the **Qmax n** field for each default cell profile. The Impedance Track™ algorithm updates these values and maintains them the associated actual cell profiles.

5.2.9 Update Status 0 and Update Status 1

Bit 0 (0x01) of the **Update Status n** registers indicates that the bq27505 has learned new Qmax parameters and is accurate. The **Update Status n** register is located at address 0x00000000 + (n * 0x00000001) + 0x00000000. The **Update Status n** register is located at address 0x00000000 + (n * 0x00000001) + 0x00000000.

5.2.13 Default Ra and Ra Tables

These tables contain encoded data and, with the exception of the **Default Ra Tables**, are automatically updated during device operation. No user changes should be made except for reading/writing the values from a pre-learned pack (part of the process for creating golden image files).

5.3 DETAILED PIN DESCRIPTION

5.3.1 The Operation Configuration Register

Some bq27505 pins are configured via the **Operation Configuration** data flash register, as indicated in [Table 5-3](#). This register is programmed/read via the methods described in [Section 4.2.1](#), Accessing the Data Flash. The register is located at subclass = 64, offset = 0.

Table 5-3. Operation Configuration Bit Definition

	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
High byte	RESCAP	BATG_OVR	INT_BREM	PFC_CFG1	PFC_CFG0	IWAKE	RSNS1	RSNS0
Low byte	INT_FOCV	IDSELEN	SLEEP	RMFCC	SOCI_POL	BATG_POL	BATL_POL	TEMPS

RESCAP = No-load rate of compensation is applied to the reserve capacity calculation. True when set. Default is 0.

BATG_OVR = BAT_GD override bit. If the gauge enters Hibernate only due to the cell voltage, the BAT_GD will not negate. True when set. Default is 0.

INT_BREM = Battery removal interrupt bit. The SOC_INT pulses 1ms when the battery removal interrupt is enabled. True when set. The default is 0.

PFC_CFG1/PFC_CFG0 = Pin function code (PFC) mode selection: PFC 0, 1, or 2 selected by 0/0, 0/1, or 1/0, respectively. Default is PFC 1 (0/1).

IWAKE/RSNS1/RSNS0 = These bits configure the current wake function (see [Table 5-6](#)). Default is 0/0/1.

INT_FOCV = Indication of the measurement of the OCV during the initialization. The SOC_INT will pulse during the first measurement if this bit is set. True when set. Default is 0.

IDSELEN = Enables cell profile selection feature. True when set. Default is 1.

SLEEP = The fuel gauge can enter sleep, if operating conditions allow. True when set. Default is 1.

RMFCC = RM is updated with the value from FCC, on valid charge termination. True when set. Default is 1.

SOCI_POL = SOC interrupt polarity is active-low. True when cleared. Default is 0.

BATG_POL = BAT_GD pin is active-low. True when cleared. Default is 0.

BATL_POL = BAT_LOW pin is active-high. True when set. Default is 1.

TEMPS = Selects external thermistor for *Temperature()* measurements. True when set. Default is 1.

Some bq27505 pins are configured via the **Operation Configuration B** data flash register, as indicated in [Table 5-4](#). This register is programmed/read via the methods described in [Section 4.2.1](#): Accessing the Data Flash. The register is located at subclass = 64, offset = 9.

Table 5-4. Operation Configuration B Bit Definition

bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
WRTEMP	BIE	BL_INT	GNDSEL	BG_INIT	DFWIndBL	–	–

WRTEMP = Enables the temperature write. The temperature could be written by the host. True when set. Default is 0.

BIE = Battery insertion detection enable. When the battery insertion detection is disabled, the gauge relies on the host command to set the BAT_DET bit. True when set. Default is 1.

BL_INT = Battery low interrupt enable. True when set. Default is 0.

GNDSEL = The ADC ground select control. The Vss (Pin D1) is selected as ground reference when the bit is clear. Pin A1 is selected when the bit is set. Default is 1.

BG_INIT = BAT_GD will be asserted during the initialization. It is for application that needs the system be powered up ASAP. True when set. Default is 0.

DFWIndBL = DataFlash Write Indication. SOC_INT is used for indication if the bit is clear. BAT_LOW is used for indication if the bit is set. Default is 0.

5.3.2 Pin Function Code Descriptions

The bq27505 has three possible pin-function variations that can be selected in accordance with the circuit architecture of the end application. Each variation has been assigned a pin function code, or PFC.

When the PFC is set to 0, only the bq27505 measures battery temperature under discharge and relaxation conditions. The charger does not receive any information from the bq27505 about the temperature readings, and therefore operates open-loop with respect to battery temperature.

A PFC of 1 is like a PFC of 0, except temperature is also monitored during battery charging. If charging temperature falls outside of the preset range defined in data flash, a charger can be disabled via the BAT_GD pin until cell temperature recovers. See [Section 5.6.2, Charge Inhibit and Suspend](#), for additional details.

Finally when the PFC is set to 2, the battery thermistor can be shared between the fuel gauge and the charger. The charger has full usage of the thermistor during battery charging, while the fuel gauge uses the thermistor exclusively during discharge and battery relaxation.

When PFC = 0 or 2, the bq27505 must be queried by the system in order to determine the battery temperature. At that time, the bq27505 samples the temperature. This saves battery energy when operating from battery, as periodic temperature updates are avoided during charging mode.

The PFC is specified in **Operation Configuration [PFC_CFG1, PFC_CFG0]**. The default is PFC = 1.

5.3.3 BAT_LOW Pin

The BAT_LOW pin provides a system processor with an electrical indicator of battery status. The signaling on the BAT_LOW pin follows the status of the [SOC1] bit in the *Flags()* register. Note that the polarity of the BAT_LOW pin can be inverted via the [BATL_POL] bit of **Operation Configuration**.

5.3.4 Power Path Control With the BAT_GD Pin

The bq27505 must operate in conjunction with other electronics in a system appliance, such as chargers or other ICs and application circuits that draw appreciable power. After a battery is inserted into the system, there should be no charging current or a discharging current higher than C/20, so that an accurate OCV can be read. The OCV is used for helping determine which battery profile to use, as it constitutes part of the battery impedance measurement

When a battery is inserted into a system, the Impedance Track™ algorithm requires that no charging of the battery takes place and that any discharge is limited to less than C/20—these conditions are sufficient for the fuel gauge to take an accurate OCV reading. To disable these functions, the BAT_GD pin is merely negated from the default setting. Once an OCV reading has been made, the BAT_GD pin is asserted, thereby enabling battery charging and regular discharge of the battery. The **Operation Configuration [BATG_POL]** bit can be used to set the polarity of the battery good signal, should the default configuration need to be changed.

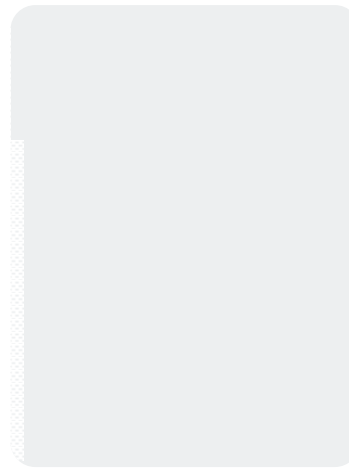


Figure 5-1. Power Mode Diagram

Figure 5-1 details how the BAT_GD pin functions in the context of battery insertion and removal, as well as NORMAL vs. SLEEP modes.

In PFC 1, the BAT_GD pin is also used to disable battery charging when the bq27505 reads battery temperatures outside the range defined by [**Charge Inhibit Temp Low, Charge Inhibit Temp High**]. The BAT_GD line is asserted once temperature falls within the range [**Charge Inhibit Temp Low + Temp Hys, Charge Inhibit Temp High – Temp Hys**].

5.3.5 Battery Detection Using the BI/TOUT Pin

During power-up or hibernate activities, or any other activity where the bq27505 needs to determine whether a battery is connected or not, the fuel gauge applies a test for battery presence. First, the BI/TOUT pin is put into high-Z status. The weak 1.8MΩ pull-up resistor will keep the pin high while no battery is present. When a battery is inserted (or is already inserted) into the system device, the BI/TOUT pin will be pull(no)Tjj 34.98

5.3.6 SOC_INT pin

The SOC_INT pin generates a pulse with different pulse width under various conditions. Some features needs to be enabled by setting the Operation Config. In any given one second, only one SOC_INT pulse could be generated. In other words, the 1ms SOC_INT pulse could indicated multiple events generating the 1ms pusles.

Table 5-5. SOC_INT Pulse Condition and Width

	Enable Condition	Pulse Width	Comment
SOC_Delta Point	SOC_Delta \neq 0	1 ms	During charge, when the SOC is greater than ($>$) the points, $100\% - n \times \text{SOC_Delta}$ and 100% ; During discharge, when the SOC reaches (\leq) the points $100\% - n \times \text{SOC_Delta}$ and 0% ; where n is an integer starting from 0 to the number generating SOC no less than 0%
SOC1 Set	Always	1 ms	When RSOC reached the SOC1 Set or Clear threshold set in the Data Flash and BL_INT bit in Operation Configuration B is set.
SOC1 Clear	Always	1 ms	
SysDown Set	Always	1 ms	When the Battery Voltage reached the SysDown Set or Clear threshold set in the Data Flash
SysDown Clear	Always	1 ms	
State Change	SOC_Delta \neq 0	1 ms	When there is a state change including charging, discharging and relaxation. This function is disabled when SOC_Delta is set to 0.
Battery Removal	INT_BREM bit is set in OpConfig AND BIE bit is set	1ms	This function is disabled when BIE is cleared.
OCV Command	After Initialization	About 165ms. Same as the OCV command execution time period	SOC_INT pulses for the OCV command after the initialization.
OCV Command	INT_FOCV bit is set in OpConfig	About 165ms. Same as the OCV command execution time period	This command is to generate the SOC_INT pulse during the initialization.
Data Flash Write	After Initialization AND DFWrIndWaitTime \neq 0	Programmable time pluse flash erase and write time	SOC_INT is used to indicate the data flash update. The gauge will wait <i>DFWrIndWaitTime times 5ms</i> after the SOC_INT signal to start the data flash update. This function is disabled if DFWrIndWaitTime is set to 0.

5.4 TEMPERATURE MEASUREMENT

The bq27505 measures battery temperature via its TS input, in order to supply battery temperature status information to the fuel gauging algorithm and charger-control sections of the gauge. Alternatively, it can also measure internal temperature via its on-chip temperature sensor, but only if the **[TEMPS]** bit of the **Operation Configuration** register is cleared. The **[GNDSEL]** bit of Operation Configuration B register selects the ground reference of the ADC converter for temperature measurement.

Regardless of which sensor is used for measurement, a system processor can request the current battery temperature by calling the *Temperature()* function (see [Section 4.1.1, Standard Data Commands](#), for specific information).

The thermistor circuit requires the use of an external NTC 103AT-type thermistor. Additional circuit information for connecting this thermistor to the bq27505 is shown in [Section 8, Reference Schematic](#).

5.5 OVERTEMPERATURE INDICATION

5.5.1 Overtemperature: Charge

If during charging, *Temperature()* reaches the threshold of **OT Chg** for a period of **OT Chg Time** and *AverageCurrent()* > **Chg Current Threshold**, then the **[OTC]** bit of *Flags()* is set. When *Temperature()* falls to **OT Chg Recovery**, the **[OTC]** of *Flags()* is reset.

If **OT Chg Time** = 0, then the feature is completely disabled.

5.5.2 Overtemperature: Discharge

If during discharging, *Temperature()* reaches the threshold of **OT Dsg** for a period of **OT Dsg Time**, and *AverageCurrent()* ≤ **-Dsg Current Threshold**, then the **[OTD]** bit of *Flags()* is set. When *Temperature()* falls to **OT Dsg Recovery**, the **[OTD]** bit of *Flags()* is reset.

If **OT Dsg Time** = 0, then feature is completely disabled.

5.6 CHARGING AND CHARGE-TERMINATION INDICATION

5.6.1 Detecting Charge Termination

For proper bq27505 operation, the cell charging voltage must be specified by the user. The default value for this variable is **Charging Voltage** [Table 4-7](#).

The bq27505 detects charge termination when (1) during 2 consecutive periods of **Current Taper Window**, the *AverageCurrent()* is < **Taper Current**, (2) during the same periods, the accumulated change in capacity > Min Taper Charge / **Current Taper Window**, and (3) *Voltage()* > **Charging Voltage – Taper Voltage**. When this occurs, the **[CHG]** bit of *Flags()* is cleared. Also, if the **[RMFCC]** bit of **Operation Configuration** is set, then *RemainingCapacity()* is set equal to *FullChargeCapacity()*.

5.6.2 Charge Inhibit and Suspend

The bq27505 can indicate when battery temperature has fallen below or risen above predefined thresholds **Charge Inhibit Temp Low** or **Charge Inhibit Temp High**, respectively. In this mode, the **[CHG_INT]** bit is set and the BAT_GD pin is deserted to indicate this condition. The **[CHG_INT]** bit is cleared and the BAT_GD pin is asserted once the battery temperature returns to the range **[Charge Inhibit Temp Low + Temp Hys, Charge Inhibit Temp High – Temp Hys]**.

When PFC = 1, the bq27505 can indicate when battery temperature has fallen below or risen above predefined thresholds **Suspend Low Temp** or **Suspend High Temp**, respectively. In this mode, the **[XCHG]** bit is set to indicate this condition. The **[XCHG]** bit is cleared once the battery temperature returns to the range **[Charge Inhibit Temp Low + Temp Hys, Charge Inhibit Temp High – Temp Hys]**.

The charging should not start when the temperature is below the Charge Inhibit Temp Low or above the Charge Inhibit Temp High. The charging can continue if the charging starts inside the window [Charge Inhibit Temp Low, Charge Inhibit Temp High] until the temperature is either below Suspend Low Temp or above the Suspend Low Temp. Therefore, the window [Charge Inhibit Temp Low, Charge Inhibit Temp High] must be inside the window of [Suspend Low Temp, Suspend High Temp].

5.7 POWER MODES

The bq27505 has different power modes: BAT INSERT CHECK, NORMAL, SLEEP, SLEEP+ and HIBERNATE. In NORMAL mode, the bq27505 is fully powered and can execute any allowable task. In SLEEP+ mode, both low frequency and high frequency oscillators are active. Although the SLEEP+ has higher current consumption than the SLEEP mode, it is also a reduced power mode. In SLEEP mode, the fuel gauge turns off the high frequency oscillator and exists in a reduced-power state, periodically taking measurements and performing calculations. In HIBERNATE mode, the fuel gauge is in a very low power state, but can be woken up by communication or certain I/O activity. Finally, the BAT INSERT CHECK mode is a powered-up, but low-power halted, state, where the bq27505 resides when no battery is inserted into the system.

The relationship between these modes is shown in [Figure 5-1](#).

5.7.1 BAT INSERT CHECK Mode

This mode is a halted-CPU state that occurs when an adapter, or other power source, is present to power the bq27505 (and system), yet no battery has been detected. When battery insertion is detected, a series of initialization activities begin, which include: OCV measurement, setting the BAT_GD pin, and selecting the appropriate battery profiles.

Some commands, issued by a system processor, can be processed while the bq27505 is halted in this mode. The gauge will wake up to process the command, then return to the halted state awaiting battery insertion.

5.7.2 NORMAL MODE

The fuel gauge is in NORMAL mode when not in any other power mode. During this mode, *AverageCurrent()*, *Voltage()* and *Temperature()* measurements are taken, and the interface data set is updated. Decisions to change states are also made. This mode is exited by activating a different power mode.

Because the gauge consumes the most power in NORMAL mode, the Impedance Track™ algorithm minimizes the time the fuel gauge remains in this mode.

5.7.3 SLEEP MODE

SLEEP mode is entered automatically if the feature is enabled (**Operation Configuration [SLEEP] = 1**) and *AverageCurrent()* is below the programmable level **Sleep Current**. Once entry into SLEEP mode has been qualified, but prior to entering it, the bq27505 performs an coulomb counter autocalibration to minimize offset.

During SLEEP mode, the bq27505 periodically takes data measurements and updates its data set. However, a majority of its time is spent in an idle condition.

The bq27505 exits SLEEP if any entry condition is broken, specifically when (1) *AverageCurrent()* rises above **Sleep Current**, or (2) a current in excess of I_{WAKE} through R_{SENSE} is detected.

In the event that a battery is removed from the system while a charger is present (and powering the gauge), Impedance Track™ updates are not necessary. Hence, the fuel gauge enters a state that checks for battery insertion and does not continue executing the Impedance Track™ algorithm.

5.7.4 SLEEP+ MODE

Compared to the SLEEP mode, SLEEP+ mode has the high frequency oscillator in operation. The communication delay could be eliminated. The SLEEP+ is entered automatically if the feature is enabled (**CONTROL STATUS [SNOOZE]** = 1) and *AverageCurrent*() is below the programmable level **Sleep Current**.

During SLEEP+ mode, the bq27505 periodically takes data measurements and updates its data set. However, a majority of its time is spent in an idle condition.

The bq27505 exits SLEEP+ if any entry condition is broken, specifically when (1) any communication activity with the gauge, or (2) *AverageCurrent*() rises above **Sleep Current**, or (3) a current in excess of I_{WAKE} through R_{SENSE} is detected.

5.7.5 HIBERNATE MODE

HIBERNATE mode should be used when the system equipment needs to enter a low-power state, and minimal gauge power consumption is required. This mode is ideal when a system equipment is set to its own HIBERNATE, SHUTDOWN, or OFF modes.

Before the fuel gauge can enter HIBERNATE mode, the system must set the [**HIBERNATE**] bit of the CONTROL_STATUS register. The gauge waits to enter HIBERNATE mode until it has taken a valid OCV measurement and the magnitude of the average cell current has fallen below **Hibernate Current**. The gauge can also enter HIBERNATE mode if the cell voltage falls below **Hibernate Voltage** and a valid OCV measurement has been taken. The gauge will remain in HIBERNATE mode until the system issues a direct I²C command to the gauge or a POR occurs. I²C Communication that is not directed to the gauge will not wake the gauge.

It is important that BAT_GD be deserted status (no battery charging/discharging). This prevents a charger application from inadvertently charging the battery before an OCV reading can be taken. It is the system's responsibility to wake the bq27505 after it has gone into HIBERNATE mode. After waking, the gauge can proceed (mode)Tj n0 Tagetheresp0 0 0 rg 84 386 TT 0 zd (inadver)Tj3 0 Td (time)Tj 21.89 0 Td(resp0 0 0 rg 84 31.55 0

Table 5-6. I_{WAKE} Threshold Settings⁽¹⁾

RSNS1	RSNS0	I _{WAKE}	V _{th} (SRP–SRN)
0	0	0	Disabled
0	0	1	Disabled
0	1	0	1.0 mV or –1.0 mV
0	1	1	2.2 mV or –2.2 mV
1	0	0	2.2 mV or –2.2 mV
1	0	1	4.6 mV or –4.6 mV
1	1	0	4.6 mV or –4.6 mV
1	1	1	9.8 mV or –9.8 mV

(1) The actual resistance value vs the setting of the sense resistor is not important, just the actual voltage threshold when calculating the configuration. The voltage thresholds are typical values under room temperature.

5.8.2 FLASH UPDATES

Data Flash can only be updated if $Voltage() \geq \text{Flash Update OK Voltage}$. Flash programming current can cause an increase in LDO dropout. The value of **Flash Update OK Voltage** should be selected such that the bq27505 V_{CC} voltage does not fall below its minimum of 2.4 V during Flash write operations.

5.9 AUTOCALIBRATION

The bq27505 provides an autocalibration feature that measures the voltage offset error across SRP and SRN as operating conditions change. It subtracts the resulting offset error from normal sense resistor voltage, V_{SR} , for maximum measurement accuracy.

Autocalibration of the coulomb counter begins on entry to SLEEP mode, except if $Temperature() \leq 5^{\circ}\text{C}$ or $Temperature() \geq 45^{\circ}\text{C}$.

The fuel gauge also performs a single offset when (1) the condition of $AverageCurrent() \leq 100 \text{ mA}$ and (2) {voltage change since last offset calibration $\geq 256 \text{ mV}$ } or {temperature change since last offset calibration is greater than 80°C for $\geq 60 \text{ s}$ }.

Capacity and current measurements continue at the last measured rate during the offset calibration when these measurements cannot be performed. If the battery voltage drops more than 32 mV during the offset calibration, the load current has likely increased; hence, the offset calibration is aborted.

6 APPLICATION-SPECIFIC INFORMATION

6.1 BATTERY PROFILE STORAGE AND SELECTION

6.1.1 Common Profile Aspects

When a battery pack is removed from system equipment that implements the bq27505, the fuel gauge will maintain some of the battery information, if it is re-inserted. This way the Impedance Track™ algorithm will often have a means of recovering battery-status information; thereby, maintaining good state-of-charge (SOC) estimates.

Two default battery profiles are available to store battery information. They are used to provide the Impedance Track™ algorithm with the default information of the battery type expected to be used with the end-equipment. The bq27505 supports only one type of battery profile. This profile is stored in both the **Def0** and **Def1** profiles. Default profiles are programmed by the end-equipment manufacturer.

In addition to the default profiles, the bq27505 maintains two abbreviated profiles: *Pack0* and *Pack1*. These tables hold dynamic battery data, and keep track of the status for up to two of the most recent batteries used. In most cases the bq27505 can administrate information on two removable battery packs.

6.1.2 Activities Upon Pack Insertion

6.1.2.1 First OCV and Impedance Measurement

At power-up the BAT_GD pin is inactive, so that the system might not obtain power from the battery (this depends on actual implementation). In this state, the battery should be put in a condition with load current less than C/20. Next, the bq27505 measures its first open-circuit voltage (OCV) via the BAT pin. The [OCVMDCOMP] bit will set once the OCV measurement is completed. Depending on the load current, the [OCVFAIL] bit indicates whether the OCV reading is valid. From the OCV(SOC) table, the SOC of the inserted battery is found. Then the BAT_GD pin is made active, and the impedance of the inserted battery is calculated from the measured voltage and the load current: $Z(\text{SOC}) = (\text{OCV}(\text{SOC}) - V) / I$. This impedance is compared with the impedance of the dynamic profiles, **Packn**, and the default profiles, **Defn**, for the same SOC (the letter **n** depicts either a **0** or **1**). The [INITCOMP] bit will be set afterwards and the OCV command could be issued

6.1.3 Reading Application Status

The **Application Status** data flash location contains cell profile status information, and can be read using the *ApplicationStatus()* extended command (0x6a). The bit configuration of this function/location is shown in [Table 6-1](#).

Table 6-1. ApplicationStatus() Bit Definitions.

Application Configuration	bit7	bit6	bit5	bit4	bit3	bit2	bit1	bit0
Byte	—	—	—	—	—	—	—	LU_PROF

LU_PROF = Last profile used by fuel gauge. *Cell0* last used when cleared. *Cell1* last used when set. Default is 0.

6.2 APPLICATION-SPECIFIC FLOW AND CONTROL

The bq27505 Td dF3 Td. It (0x6a) Wj yd (B10) Tj 23.0a. 85.0a. 87.0 Td 0 Td (Then) Tj 0x6a Td (config) 80764 T(SOC)

7 COMMUNICATIONS

7.1 I²C INTERFACE

The 27505 supports the standard I²C read, incremental read, quick read, one byte write, and incremental write functions. The 7 bit device address (ADDR) is the most significant 7 bits of the hex address and is fixed as 1010101. The 8-bit device address will; therefore, be 0xAA or 0xAB for write or read, respectively.

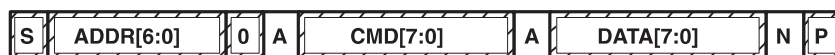
Host generated



The “quick read” returns data at the address indicated by the address pointer. The address pointer, a register internal to the I²C communication engine, will increment whenever data is acknowledged by the bq27505 or the I²C master. “Quick writes” function in the same manner and are a convenient means of sending multiple bytes to consecutive command locations (such as two-byte commands that require two bytes of data)

The following command sequences are not supported:

Attempt to write a read-only address (NACK after data sent by master):



Attempt to read an address above 0x6B (NACK command):



7.2 I²C Time Out

The I²C engine will release both SDA and SCL if the I²C bus is held low for the time defined by *I²C Timeout* times 0.5 second. If the bq27505 was holding the lines, releasing them will free for the master to drive the lines. If an external condition is holding either of the lines low, the I²C engine will enter the low power sleep mode.

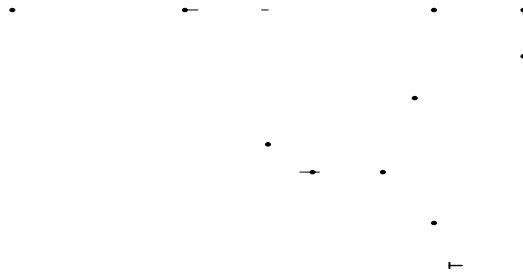
7.3 I²C Command Waiting Time

To make sure the correct results of a command with the 400KHz I²C operation, a proper waiting time should be added between issuing command and reading results. For subcommands, the following diagram shows the waiting time required between issuing the control command the reading the status with the exception of checksum and OCV commands. A 100ms waiting time is required between the checksum command and reading result, and a 1.2 second waiting time is required between the OCV command and result. For read-write standard command, a minimum of 2 seconds is required to get the result updated. For read-only standard commands, there is no waiting time required, but the host should not issue all standard commands more than two times per second. Otherwise, the gauge could result in a reset issue due to the expiration of the watchdog timer.

The I²C clock stretch could happen in a typical application. A maximum 80ms clock stretch could be observed during the flash updates. There is up to 270ms clock stretch after the OCV command is issued.

8 REFERENCE SCHEMATICS

8.1 SCHEMATIC



Revision History

NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from * Revision (11-2009) to A Revision	Page
• Changed firmware version in available options from 2.15 to 2.20	3
• Changed BattGdInit to BG_INIT in Table 5-4	27

PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
BQ27505YZGR-J3	NRND	DSBGA	YZG	12	3000	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	Replaced by BQ27505YZGR-J4
BQ27505YZGT-J3	NRND	DSBGA	YZG	12	250	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	Replaced by BQ27505YZGT-J4

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE:

TAPE AND REEL INFORMATION

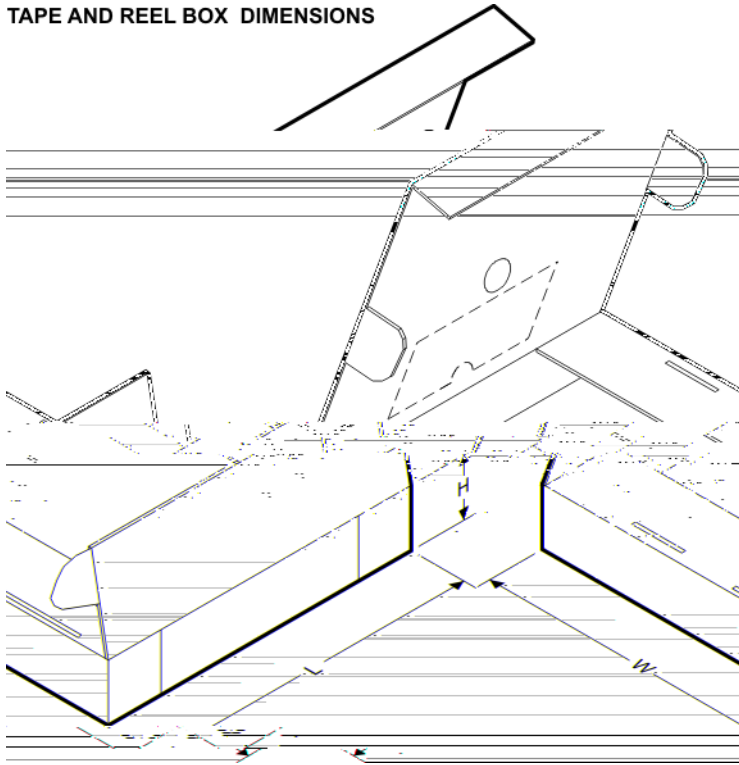
REEL DIMENSIONS

TAPE DIMENSIONS



*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BQ27505YZGR-J3	DSBGA	YZG	12	3000	210.0	185.0	35.0
BQ27505YZGT-J3	DSBGA	YZG	12	250	220.0	220.0	34.0
BQ27505YZGT-J3	DSBGA	YZG	12	250	210.0	185.0	35.0

ATING PLANE

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) balls.

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