

## Overvoltage Protection for 2-Series to 5-Series Cell Li-Ion Batteries with Internal Delay Timer

Check for Samples: [bq771800](#), [bq771801](#), [bq771802](#), [bq771803](#)

### FEATURES

- 2-, 3-, 4-, and 5-Series Cell Overvoltage Protection
- Internal Delay Timer
- Fixed OVP Threshold
- High-Accuracy Overvoltage Protection:  $\pm 10$  mV
- Low Power Consumption  $I_{CC} = 1 \mu A$  ( $V_{CELL(ALL)} < V_{PROTECT}$ )
- Low Leakage Current Per Cell Input  $< 100$  nA

- Small Package Footprint
  - 8-pin QFN (3 mm × 4 mm)

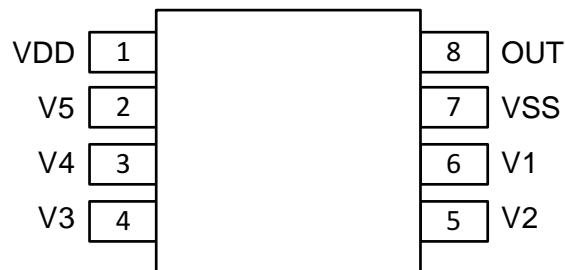
### APPLICATIONS

- Protection in Li-Ion Battery Packs in:
  - Power Tools
  - UPS Battery Backup
  - Light Electric Vehicles (eBike, eScooter, Pedal Assist Bicycles)

### DESCRIPTION

The bq7718xy family of products is an overvoltage monitor and protector for Li-Ion battery pack systems. Each cell is monitored independently for an overvoltage condition.

In the bq7718xy device, an internal delay timer is initiated upon detection of an overvoltage condition on any cell. Upon expiration of the delay timer, the output is triggered into its active state (either high or low depending on the configuration). For quicker production-line testing, the bq7718xy device provides a Customer Test Mode with greatly reduced delay time.



**Figure 1. bq771800 Pinout**



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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

### ORDERING INFORMATION

T <sub>A</sub>	Part Number	Package	Package Designator	OVP (V)	OV Hysteresis (V)	Output Delay	Output Drive	Tape and Reel (Large)	Tape and Reel (Small)
-40°C to 110°C	bq771800	8-Pin QFN	DPJ	4.300	0.300	4 s	CMOS Active High	bq771800DPJR	bq771800DPJT
	bq771801			4.275	0.050	3 s	NCH Active Low, Open Drain	bq771801DPJR	bq771801DPJT
	bq771802			4.225	0.300	1 s	NCH Active Low, Open Drain	bq771802DPJR	bq771802DPJT
	bq771803			4.275	0.050	1 s	NCH Active Low, Open Drain	bq771803DPJR	bq771803DPJT
	bq7718xy <sup>(1)</sup>			3.850–4.650	0–0.300	1 s	NCH, Active Low, Open Drain	bq7718xyDPJR	bq7718xyDPJT

(1) Future option, contact TI.

### THERMAL INFORMATION

THERMAL METRIC <sup>(1)</sup>		bq7718xy	UNITS
		8 PINS	
JA	Junction-to-ambient thermal resistance	56.6	°C/W
JC(top)	Junction-to-case(top) thermal resistance	56.4	
JB	Junction-to-board thermal resistance	30.6	
JT	Junction-to-top characterization parameter	1.0	
JB	Junction-to-board characterization parameter	37.8	
JC(bottom)	Junction-to-case(bottom) thermal resistance	11.3	

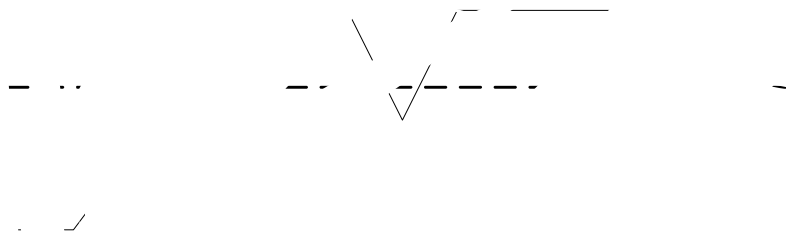
(1) For more information about traditional and new thermal metrics, see the *IC Package Thermal Metrics* application report, [SPRA953](#).

## PIN FUNCTIONS

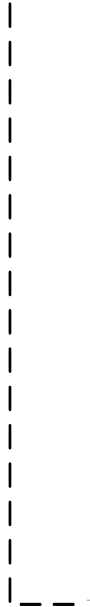
bq7718xy	Pin Name	Type I/O	Description
1	VDD	P	Power supply
2	V5	I	Sense input for positive voltage of the fifth cell from the bottom of the stack
3	V4	I	Sense input for positive voltage of the fourth cell from the bottom of the stack
4	V3	I	Sense input for positive voltage of the third cell from the bottom of the stack
5	V2	I	Sense input for positive voltage of the second cell from the bottom of the stack
6	V1	I	Sense input for positive voltage of the lowest cell in the stack
7	VSS	P	Electrically connected to IC ground and negative terminal of the lowest cell in the stack
8	OUT	O	Output drive for overvoltage fault signal

## PIN DETAILS

In the bq7718xy device, each cell is monitored independently. Overvoltage is detected by comparing the actual cell voltage to a protection voltage reference,  $V_{OV}$ . If any cell voltage exceeds the programmed OV value, a timer



## FUNCTIONAL BLOCK DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

Over operating free-air temperature range (unless otherwise noted)<sup>(1)</sup>

PARAMETER	CONDITION	VALUE/UNIT
Supply voltage range	VDD–VSS	–0.3 to 30 V
Input voltage range	V5–VSS or V4–VSS or V3–VSS or V2–VSS or V1–VSS	–0.3 to 30 V
Output voltage range	OUT–VSS	–0.3 to 30 V
Continuous total power dissipation, P <sub>TOT</sub>		See package dissipation rating.
Functional temperature		–40 to 110°C
Storage temperature range, T <sub>STG</sub>		–65 to 150°C
Lead temperature (soldering, 10 s), T <sub>SOLDER</sub>		300°C

(1) Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

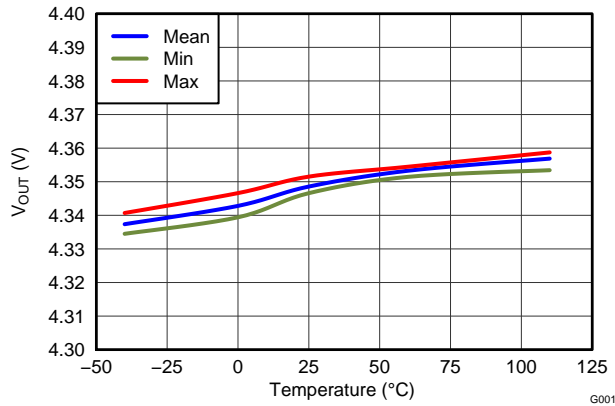
## RECOMMENDED OPERATING CONDITIONS

Over operating free-air temperature range (unless otherwise noted)

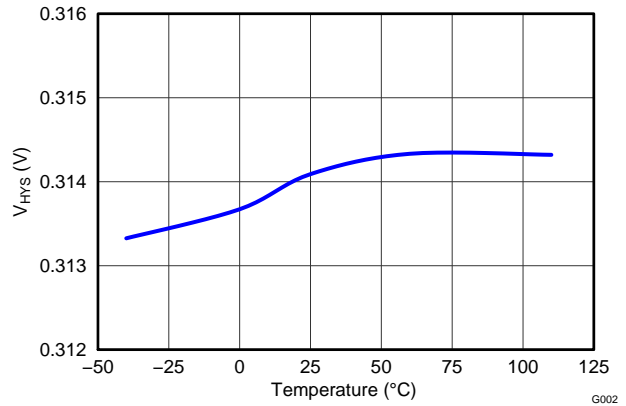
PARAMETER	MIN	NOM	MAX	UNIT
Supply voltage, V <sub>DD</sub> <sup>(1)</sup>	3		25	V
Input voltage range				

$= (V3-V2) = (V2-V1) = I_{cc}$

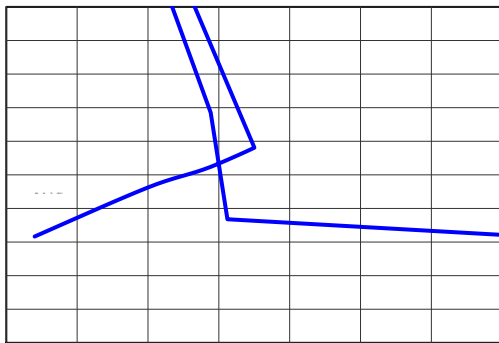
**TYPICAL CHARACTERISTICS**



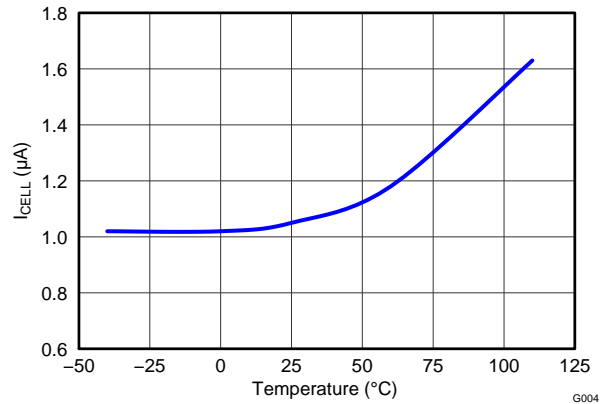
**Figure 3. Overtolerance Threshold (OVT) vs. Temperature**



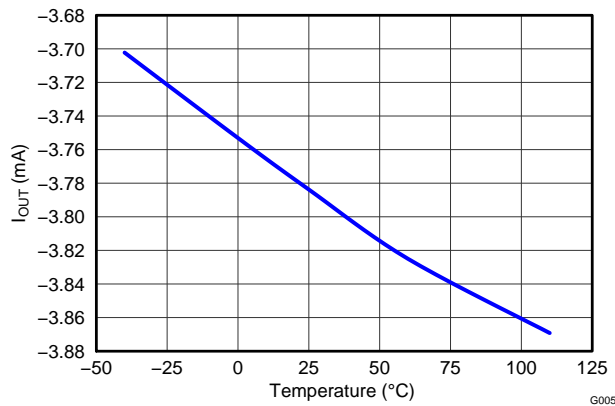
**Figure 4. Hysteresis  $V_{HYS}$  vs. Temperature**



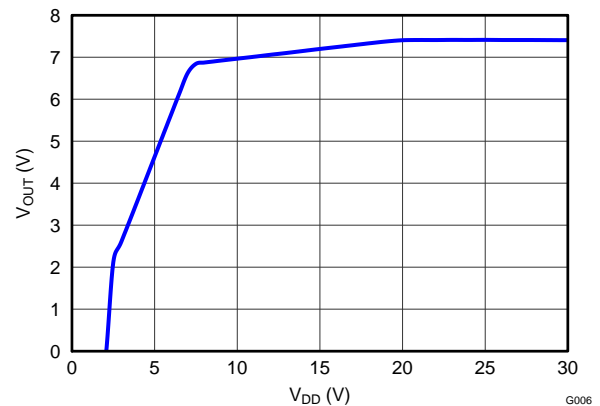
**Figure 5.  $I_{DD}$  Current Consumption vs. Temperature at  $V_{DD} = 16\text{ V}$**



**Figure 6.  $I_{CELL}$  vs. Temperature at  $V_{CELL} = 9.2\text{ V}$**



**Figure 7. Output Current  $I_{OUT}$  vs. Temperature**



**Figure 8.  $V_{OUT}$  vs.  $V_{DD}$**

APPLICATION INFORMATION

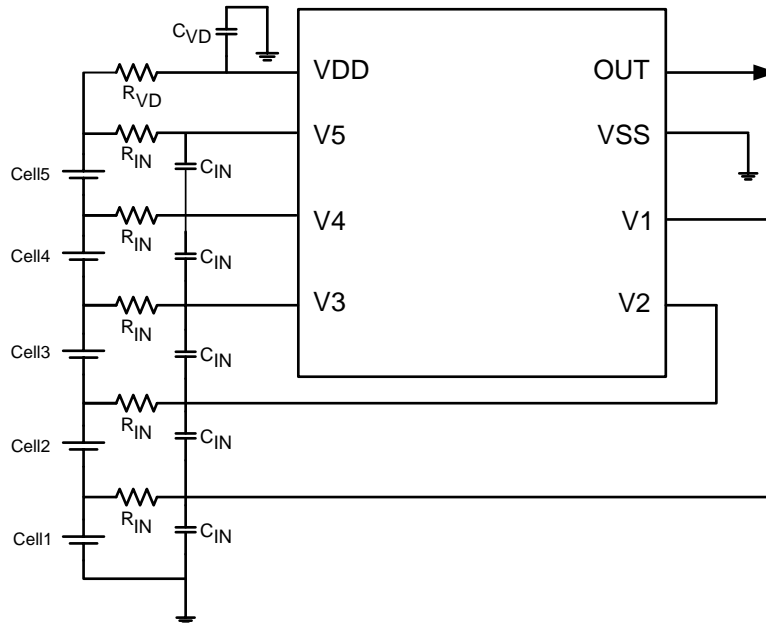


Figure 9. Application Configuration

NOTE

In the case of an Open Drain Active Low configuration, an external pull-up resistor is required on the OUT terminal.

Changes to the ranges stated in Table 1 will impact the accuracy of the cell measurements.

Changes to the ranges stated in Table 1 will impact the accuracy of the cell measurements. Figure 9 shows each external component.

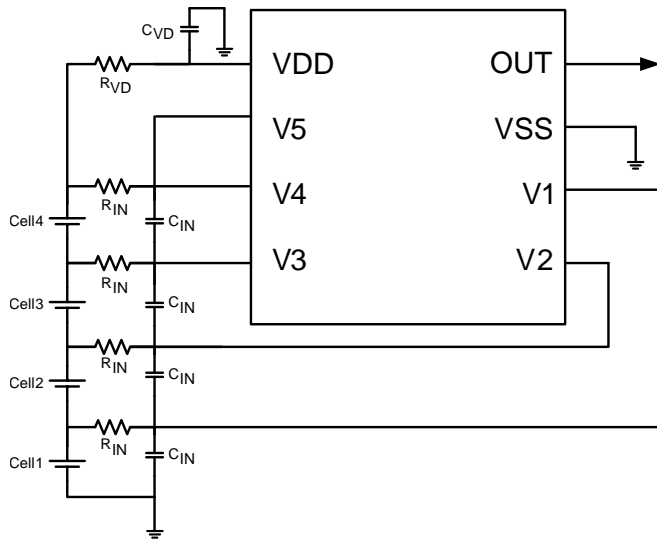
Table 1. Parameters

PARAMETER	EXTERNAL COMPONENT	MIN	NOM	MAX	UNIT
Voltage monitor filter resistance	R <sub>IN</sub>	900	1000	1100	
Voltage monitor filter capacitance	C <sub>IN</sub>	0.01		0.1	μF
Supply voltage filter resistance	R <sub>VD</sub>	100		1K	
Supply voltage filter capacitance	C <sub>VD</sub>		0.1		μF
CD external delay capacitance			0.1	1	μF
OUT Open drain version pull-up resistance to PACK+			100		k

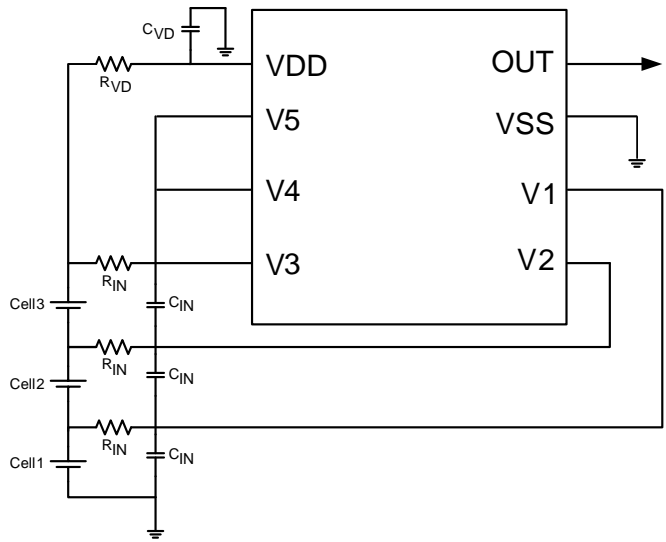
NOTE

The device is calibrated using an R<sub>IN</sub> value = 1 k . Using a value other than this recommended value changes the accuracy of the cell voltage measurements and V<sub>OV</sub> trigger level.

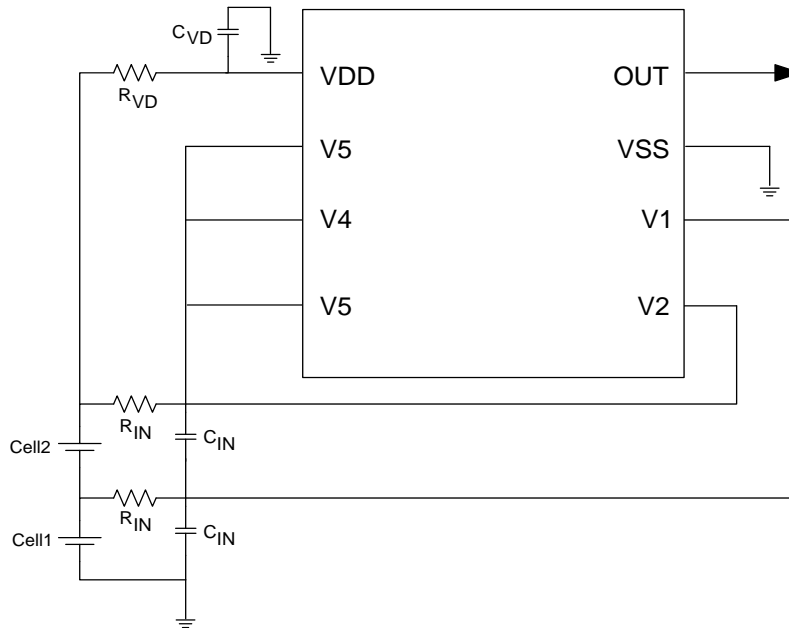
**APPLICATION SCHEMATIC**



**Figure 10. 4-Series Cell Configuration**



**Figure 11. 3-Series Cell Configuration with Fixed Delay**



**Figure 12. 2-Series Cell Configuration with Internal Fixed Delay**

**NOTE**

In these application examples, an external pull-up resistor is required on the OUT terminal to configure for an Open Drain Active Low operation.



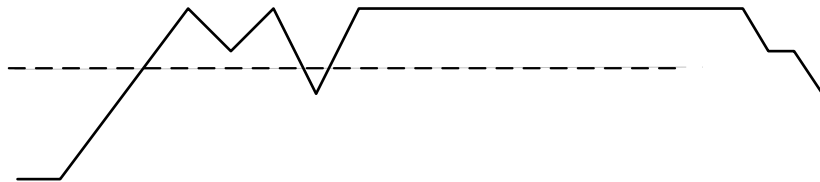
## CUSTOMER TEST MODE

Customer Test Mode (CTM) helps to reduce test time for checking the overvoltage delay timer parameter once the circuit is implemented in the battery pack. To enter CTM, VDD should be set to at least 10 V higher than V5 (see [Figure 13](#)). The delay timer is greater than 10 ms, but considerably shorter than the timer delay in normal operation. To exit Customer Test Mode, remove the VDD to V5 voltage differential of 10 V so that the decrease in this value automatically causes an exit.

### CAUTION

Avoid exceeding any Absolute Maximum Voltages on any pins when placing the part into Customer Test Mode. Also avoid exceeding Absolute Maximum Voltages for the individual cell voltages (V5–V4), (V4–V3), (V4–V3), (V3–V2), (V2–V1), and (V1–VSS). Stressing the pins beyond the rated limits may cause permanent damage to the device.

[Figure 13](#) shows the timing for the Customer Test Mode.



**Figure 13. Timing for Customer Test Mode**

Figure 14 shows the measurement for current consumption for the product for both VDD and Vx.

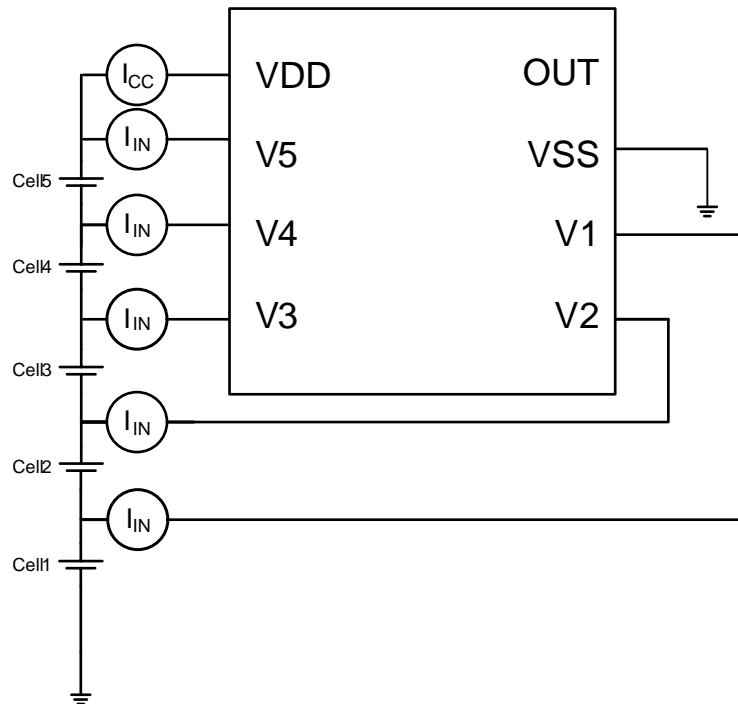
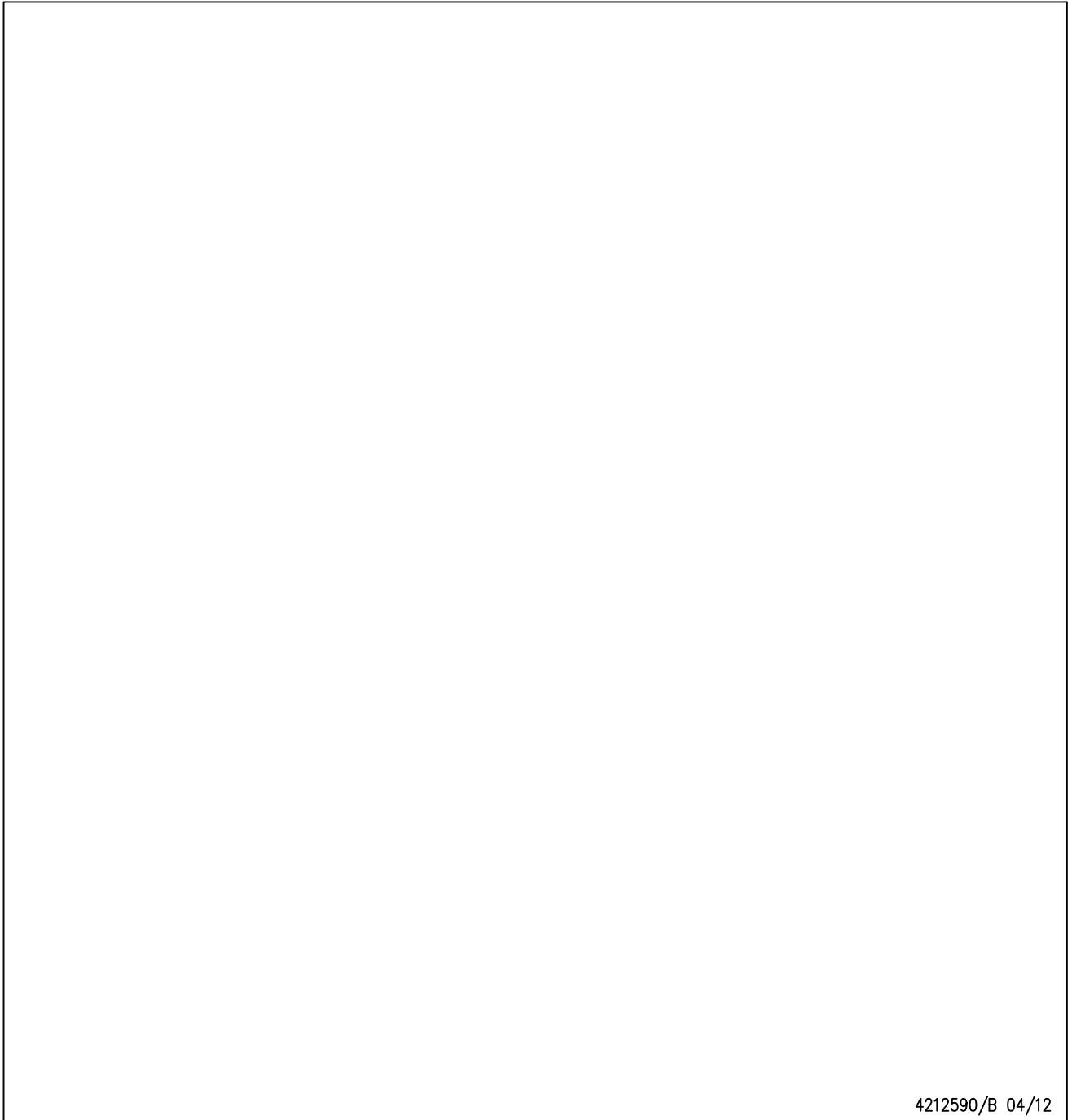


Figure 14. Configuration for IC Current Consumption Test

**ME :**

DPJ (R-PWSON-N8)

PLASTIC SMALL OUTLINE NO-LEAD



4212590/B 04/12

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Op Temp (°C)	Top-Side Markings (4)	Samples
BQ771800DPJR	ACTIVE	WSON	DPJ	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR		771800	<a href="#">Samples</a>
BQ771800DPJT	ACTIVE	WSON	DPJ	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR		771800	<a href="#">Samples</a>
BQ771801DPJR	ACTIVE	WSON	DPJ	8	3000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR		771801	<a href="#">Samples</a>
BQ771801DPJT	ACTIVE	WSON	DPJ	8	250	Green (RoHS & no Sb/Br)					

<sup>(4)</sup> Only one of markings shown within the brackets will appear on the physical device.

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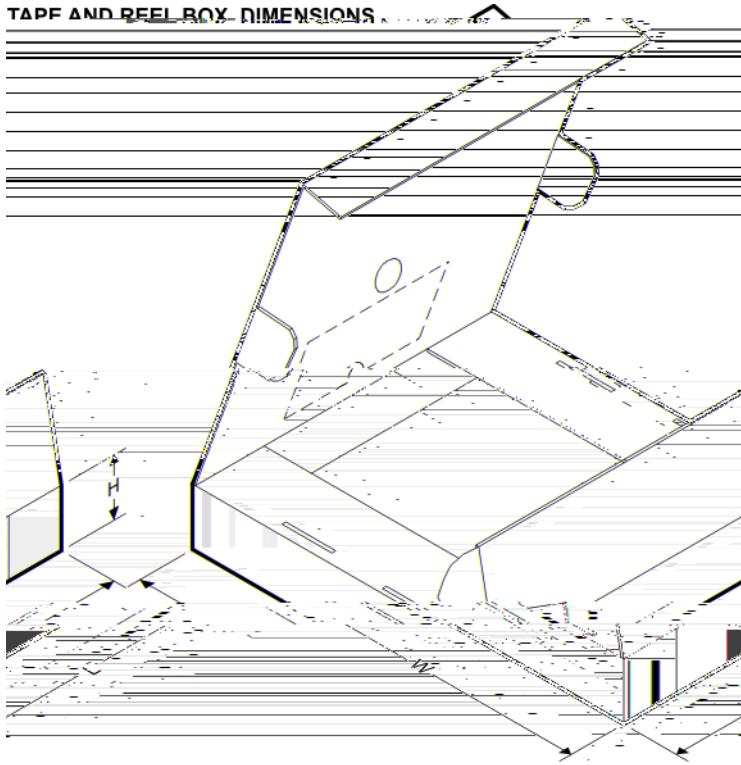
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**TAPE AND REEL INFORMATION**


\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
BQ771800DPJR	WSON	DPJ	8	3000	330.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2
BQ771800DPJT	WSON	DPJ	8	250	180.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2
BQ771801DPJR	WSON	DPJ	8	3000	330.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2
BQ771801DPJT	WSON	DPJ	8	250	180.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2
BQ771802DPJR	WSON	DPJ	8	3000	330.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2
BQ771802DPJT	WSON	DPJ	8	250	180.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2
BQ771803DPJR	WSON	DPJ	8	3000	330.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2
BQ771803DPJT	WSON	DPJ	8	250	180.0	12.4	3.3	4.3	1.1	8.0	12.0	Q2

**TAPE AND REEL BOX DIMENSIONS**



\*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
BQ771800DPJR	WSON	DPJ	8	3000	367.0	367.0	35.0
BQ771800DPJT	WSON	DPJ	8	250	210.0	185.0	35.0
BQ771801DPJR	WSON	DPJ	8	3000	367.0	367.0	35.0

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